Instructor: Dr. Saeid Belkasim  
Suite 745  
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Office Hours: 2:00 – 3:00 p.m. TR, others by appointment.


Course Description: Logic design, combinational and sequential circuits, input-output devices, memory, processors, controllers, parallel architectures, bit-slicing, and reduced instruction sets. (Refer to GSU Catalog description).

Prerequisites*: CSC 3210. (Grade C or higher)
Requirements: Students are expected to attend all classes. Regular completion of all assignments, quizzes and tests is absolutely essential to succeed in this course.

Grading:  
CSC4210  
Quizzes: (15%)  
Assignments: (15%)  
Mid-term tests: (35%)  
Final Exam: (35%).

CSC6210  
Quizzes: (15%)  
Assignments: (15%)  
Mid-term tests: (35%)  
Graduate Project: (35%)

The overall grade for the course is assigned according to: A+(97-100), A(94-96), A-(90-93), B+(87-89), B(84-86), B-(80-83), C+(77-79), C(74-76), C-(70-73), D(60-69), F(0-59).

Other policy:  
- Make-up’s or missed deadlines must be coordinated prior, and are allowed only at the discretion of the instructor.  
- All material submitted for grade must be the student’s own work. Any material that is not the students own work must be referenced.  
- Collaboration is allowed prior to preparation of actual material that is submitted for grade. Copying is considered a form of cheating. Each student must work individually on his or her test, homework or quiz. Any student found to be attempting to cheat on any test, quiz or homework will receive a score of 0 for that test, homework or quiz. Any attempt of getting or giving assistance in a test is considered cheating. It is the student's own responsibility to protect his or her work from being copied. No outside help is permitted. Plagiarized work is determined only by the instructor and is graded solely at the instructor’s discretion.

Test dates and locations:  
- Mid terms:  
  MT1: Thursday June 22, 2017  
  MT2: Thursday July 6, 2017  
- Final Exam: Thursday July 20, 2016 (the final test is scheduled on the last day of classes. There is no final test scheduled on the final exam week)  
  All tests are scheduled to be taken during the normal class meeting times

This syllabus provides a general plan for this course; deviations may be necessary.

* If you do not meet the prerequisite requirements, you are liable to being withdrawn from the course.